

A-1

DIVISIONAL PATENT APPLICATION TRANSMITTAL
(filed under 37 C.F.R. §1.53(b))

Attorney Docket No.: P012-1

Inventor: Charles W.C. Lin of Singapore

Title: **SUPPORT CIRCUIT WITH A TAPERED THROUGH-HOLE
FOR A SEMICONDUCTOR CHIP ASSEMBLY**

Prior Group Art Unit: 1746

Prior Examiner: A. Alanko

**Box Patent Application
Assistant Commissioner for Patents
Washington, D.C. 20231**

This patent application is a DIVISIONAL application of pending prior U.S. Application Serial No. 09/665,931, filed September 20, 2000.

Enclosed for filing with the above-identified utility patent application, please find the following:

1. Copy of the '931 application as originally filed, consisting of
 - Specification (23 pages)
 - Claims (11 pages)
 - Abstract (1 page)
 - Drawings (17 sheets) (formal)
2. Copy of Declaration from the '931 application
3. Preliminary Amendment
4. Request for Nonpublication of Application
5. Information Disclosure Statement (IDS/PTO-1449)
6. Return Postcard (MPEP 503) (*specifically itemized*)

FEE CALCULATION:

	(COL. 1) NO. FILED			(COL. 2*) NO. EXTRA	SMALL ENTITY	
					RATE	FEE
BASIC FEE:						\$370
TOTAL CLAIMS:	20	-	20	0	X \$9 =	\$0
INDEP. CLAIMS:	2	-	3	0	X \$42 =	\$0
MULTIPLE DEPENDENT CLAIMS: 0					+\$140 =	\$0
*IF THE DIFFERENCE IN COL. 2 IS LESS THAN ZERO, ENTER '0' IN COL. 2.						\$370

OTHER INFORMATION:

1. This application is entitled to small entity status. A Statement Claiming Small Entity Status by Independent Inventor was filed in the '931 application. Small entity status is still proper and desired.
2. A check in the amount of \$370 payable to the Commissioner of Patents and Trademarks is enclosed.
3. The Power of Attorney to David M. Sigmond appears in the '931 application.
4. This application and the '931 application are owned by Charles W.C. Lin.
5. Correspondence Address:


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ADDRESSEE: BOX PATENT APPLICATION
ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

Respectfully submitted,



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Charles W.C. Lin
Title: SUPPORT CIRCUIT WITH A TAPERED THROUGH-HOLE
FOR A SEMICONDUCTOR CHIP ASSEMBLY
Serial No.: Unknown Filed: Herewith
Examiner: Unknown Group Art Unit: Unknown
Atty. Docket No.: P012-1

ASSISTANT COMMISSIONER FOR PATENTS
Washington, D.C. 20231

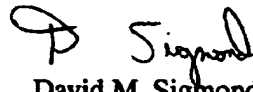
REQUEST FOR NONPUBLICATION OF APPLICATION

Applicant requests that the above-identified application not be published under 35 U.S.C. § 122(b).

Applicant certifies that the invention disclosed in this application has not been and will not be the subject of an application filed in another country, or under a multilateral international agreement, that requires publication at eighteen months after filing.

This request is being submitted with this application on filing.

Respectfully submitted,



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